

# **RJK03M4DPA**

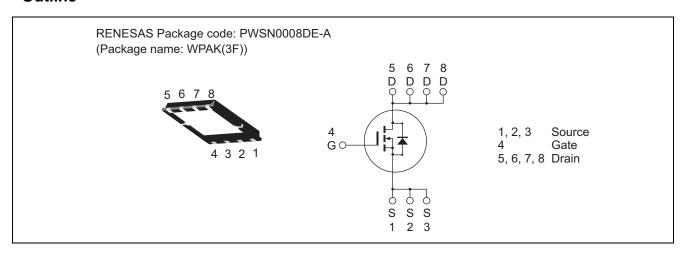
30V, 35A, 4.6mΩmax. N Channel Power MOS FET High Speed Power Switching

R07DS0768EJ0200 Rev.2.00 Feb 12, 2013

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- Pb-free
- Halogen-free

### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	35	А
Drain peak current	I <sub>D(pulse)</sub> Note1	140	А
Body-drain diode reverse drain current	I <sub>DR</sub>	35	А
Avalanche current	I <sub>AP</sub> Note 2	13	А
Avalanche energy	E <sub>AS</sub> Note 2	16.9	mJ
Channel dissipation	Pch Note3	30	W
Channel to case thermal impedance	θch-c Note3	4.16	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C

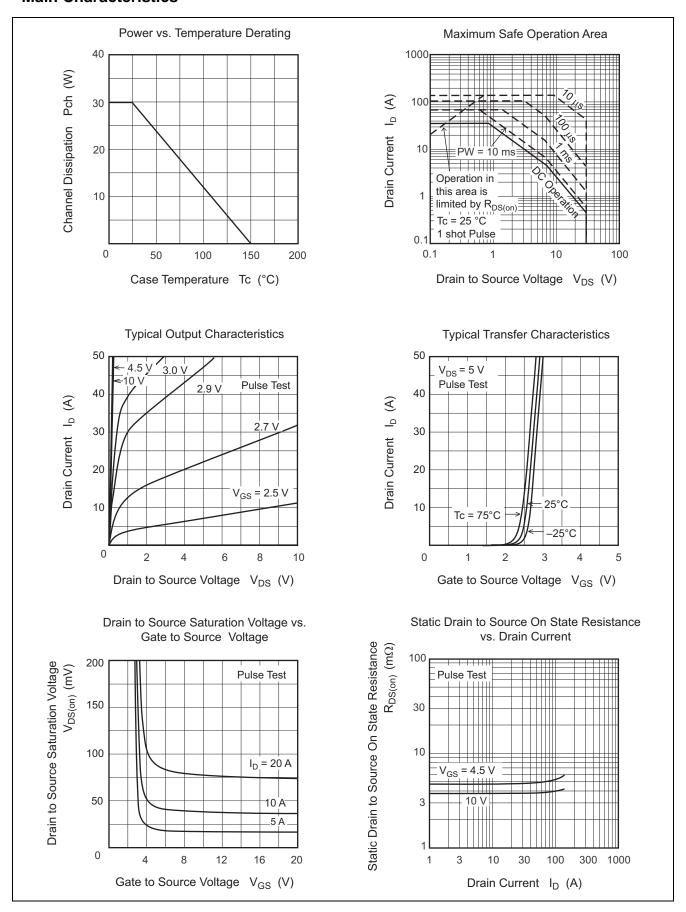
## **Electrical Characteristics**

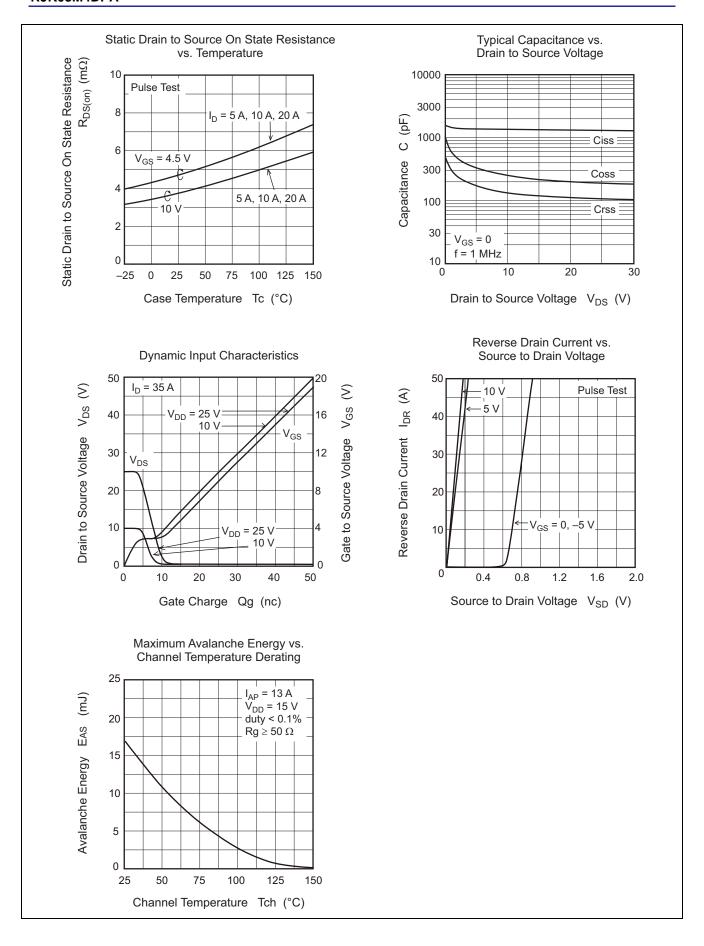
 $(Ta = 25^{\circ}C)$ 

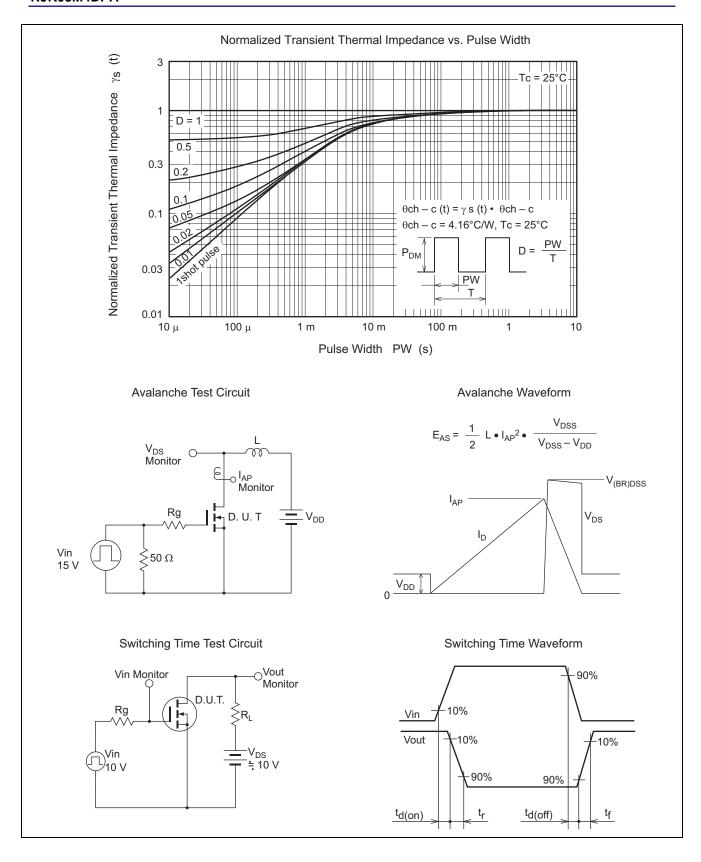
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	_	_	± 0.5	μА	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μА	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	3.8	4.6	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	4.7	6.1	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	80	_	S	$I_D = 17.5 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	1550	2170	рF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	260	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		140	_	pF	f = 1 MHz
Gate Resistance	Rg		2.0	4.0	Ω	
Total gate charge	Qg		12	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs		4.2	_	nC	V <sub>GS</sub> = 4.5 V I <sub>D</sub> = 35 A
Gate to drain charge	Qgd		3.3	_	nC	
Turn-on delay time	t <sub>d(on)</sub>		3.4	_	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17.5 A
Rise time	t <sub>r</sub>		2.4	_	ns	V <sub>DD</sub> ≅ 10 V
Turn-off delay time	$t_{d(off)}$		26	_	ns	$R_L = 0.57 \Omega$
Fall time	t <sub>f</sub>		8.3	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.09	V	$I_F = 35 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery	t <sub>rr</sub>	_	7.7	_	ns	$I_F = 35 \text{ A}, V_{GS} = 0$
time						di <sub>F</sub> / dt = 500 A/ μs

Notes: 4. Pulse test

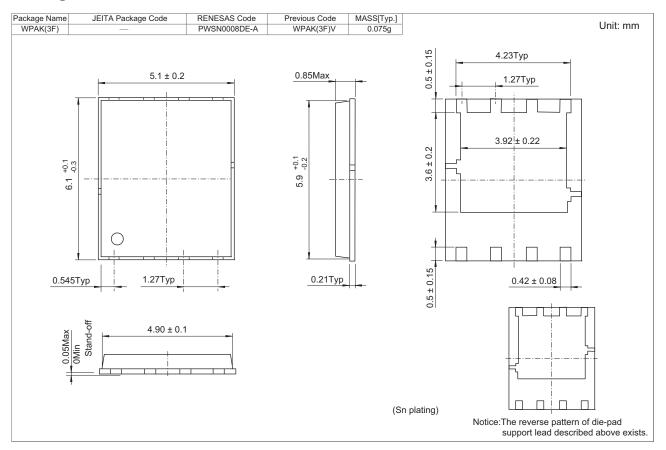
### **Main Characteristics**







### **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container			
RJK03M4DPA-00-J5A	3000 pcs	Taping			

Note: The symbol of 2nd "-" is occasionally presented as "#".

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